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Date: 10/10/02

JANET FARR  
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Janet Farr  
(Signature of person mailing paper)

Docket No: AF01120

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Applicant: Halliyal, et al

Examiner: Erik J. Kielin

Serial No.: 10/023,548

Art Unit: 2813

Filing Date: December 17, 2001

Title: **INTEGRATED ONO PROCESSING FOR SEMICONDUCTOR DEVICES USING  
IN-SITU STEAM GENERATION (ISSG) PROCESS**

SUBMISSION OF CORRECTED FORMAL DRAWINGS

Box NON-FEE AMENDMENT  
Commissioner for Patents  
Washington, D.C. 20231

Sir:

The present paper is filed together with Applicants' Reply to the Office Action mailed September 17, 2002, in response to the Examiner's point that the stored charges are in the wrong layer. The present paper submits two formal drawing sheets. Entry of the presently submitted corrected drawing sheets is requested.

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